

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MAURICIO J. SERRANO, ET AL.

Application No.: 10/725730

Filed: December 1, 2003

For: **MEMORY TRACE BUFFER**

Art Group: 2186

Examiner: Dudek, Jr., Edward J.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 19, 2007

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I hereby certify that this correspondence is being submitted electronically via EFS Web on the date shown below.

/Gayle Bekish/

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Gayle Bekish

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Under the Paperwork Reduction Act of 1995, no person is required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known

Application Number	10/725,730
Filing Date	December 1, 2003
First Named Inventor	Adl-Tabatabai, Ali-Reza
Group Art Unit	2186
Examiner Name	DUDEK, JR., Edward J.
Confirmation Number	3094

Sheet 1 of 1

Attorney Docket No: 42P17023

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		CALDER, BRAD, et al., "Cache-Conscious Data Placement", <u>Proceedings of Conference for Architecture Support for Operating Systems, ASPLOS (1998) pages 139-149</u>	
		CHILIMBI, TRISHUL M., et al., "Cache-Conscious Structure Layout", <u>Proceedings of PLDI, (1999), 12 pages</u>	
		CHILIMBI, TRISHUL M., et al., "Using Generational Garbage Collection To Implement Cache-Conscious Data Placement", <u>Proceedings of International Symposium on Memory Management, ISMM'98, (1998), 12 pages</u>	
		LIAO, STEVEN S., et al., "Post-Pass Binary Adaptation for Software-Based Speculative Precomputation", <u>Proceedings of PLDI, (2002), 12 pages</u>	
		WU, YONGFENG, et al., "Value-Profile Guided Stride Prefetching for Irregular Code", <u>Proceedings of the Compiler Construction Conference (CC2002), Grenoble, France, (2002), pages 307-324</u>	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant to place in check mark box if English language Translation is attached